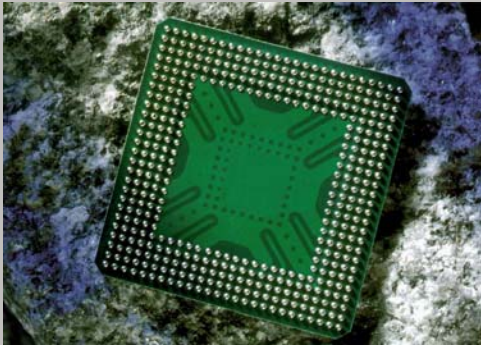


Barco Silex

IP overview



- Video and Image Processing
- Memory Control
- Crypto and Security
- DO-254

Barco Silex design expertise is made available through a wide portfolio of IP products, with a strong focus on high performance standardized image processing, memory control and encryption functions. All these IP cores have been designed and fully validated by Barco Silex and are hardware proven, which guarantees high IP quality as well as best support during your integration phase.

Deliverables include:

- RTL code; netlist (depending on license type)
- Functional simulation testbench
- Synthesis and implementation scripts
- Full documentation

For most of the IPs, we can also provide simulation models and design kit. These off-the-shelf, high quality IP cores provide you with the fastest and most efficient way of integrating complex functionalities on FPGAs or ASICs, while meeting short time to market constraints.

Video and image processing

HD/DCI JPEG 2000 decoder and encoder	BA109/B1110
Sub-frame latency JPEG 2000 decoder and encoder	BA129/BA130
JPEG color decoder and encoder	BA115/BA116

Memory control

Multi-Port AHB DDR SDRAM controller	BA312
Single-Port AHB SRAM / Nor Flash controller	BA313A
Single-Port AHB Nand Flash Controller with ECC	BA315A
AXI DDR2 SDRAM controller	BA316
Multi-port DDR2/DDR3 SDRAM controller	BA317
EBI - External Bus interface	BA318
AHB multi-channel DMA controller	BA612A

Crypto / Security

Multi-purpose AES crypto engine (enc/dec)	BA411E
DES/3DES (enc/dec)	BA412
Hashing IP core	BA413
Public Key crypto engine	BA414E

DO-254

ARINC 429 Rx/Tx (DO-254 compliant)	BA511
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